FIG.1

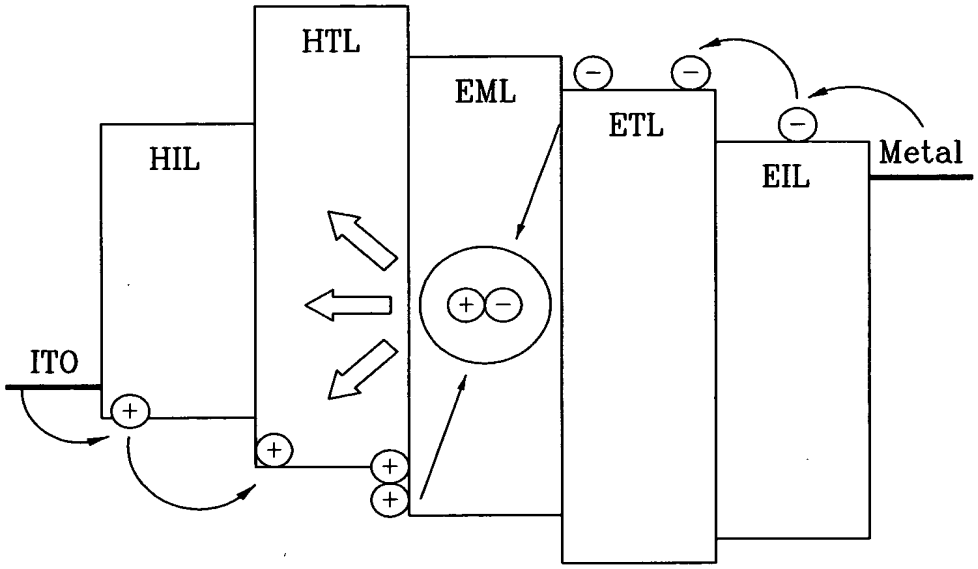


FIG.2

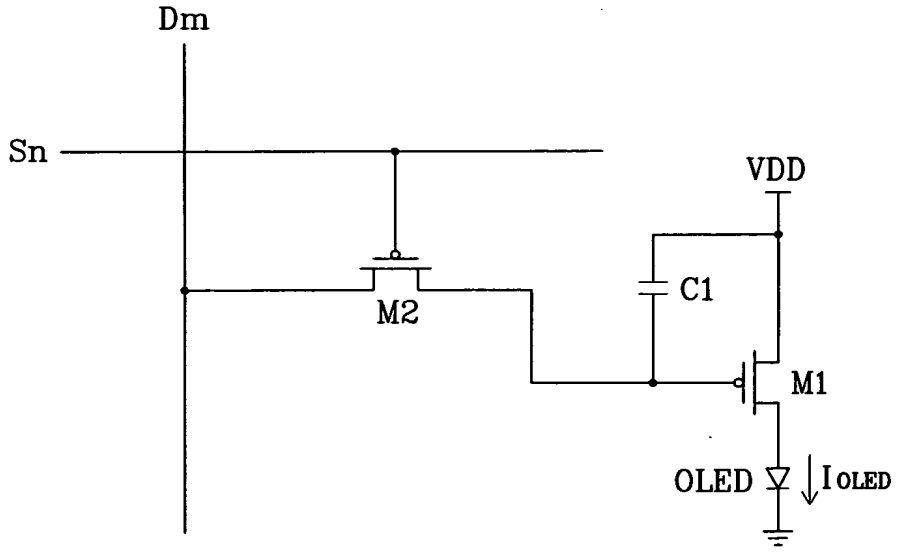


FIG.3

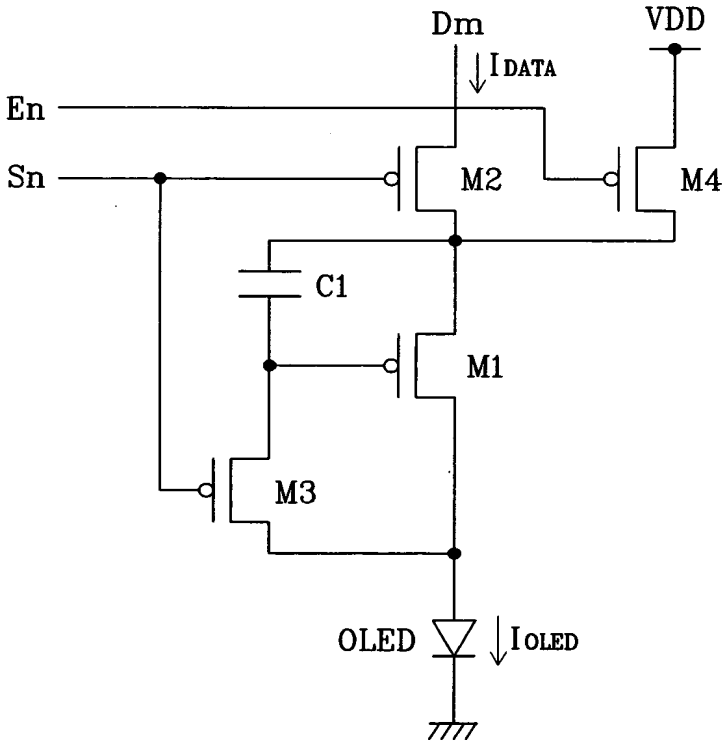


FIG. 4

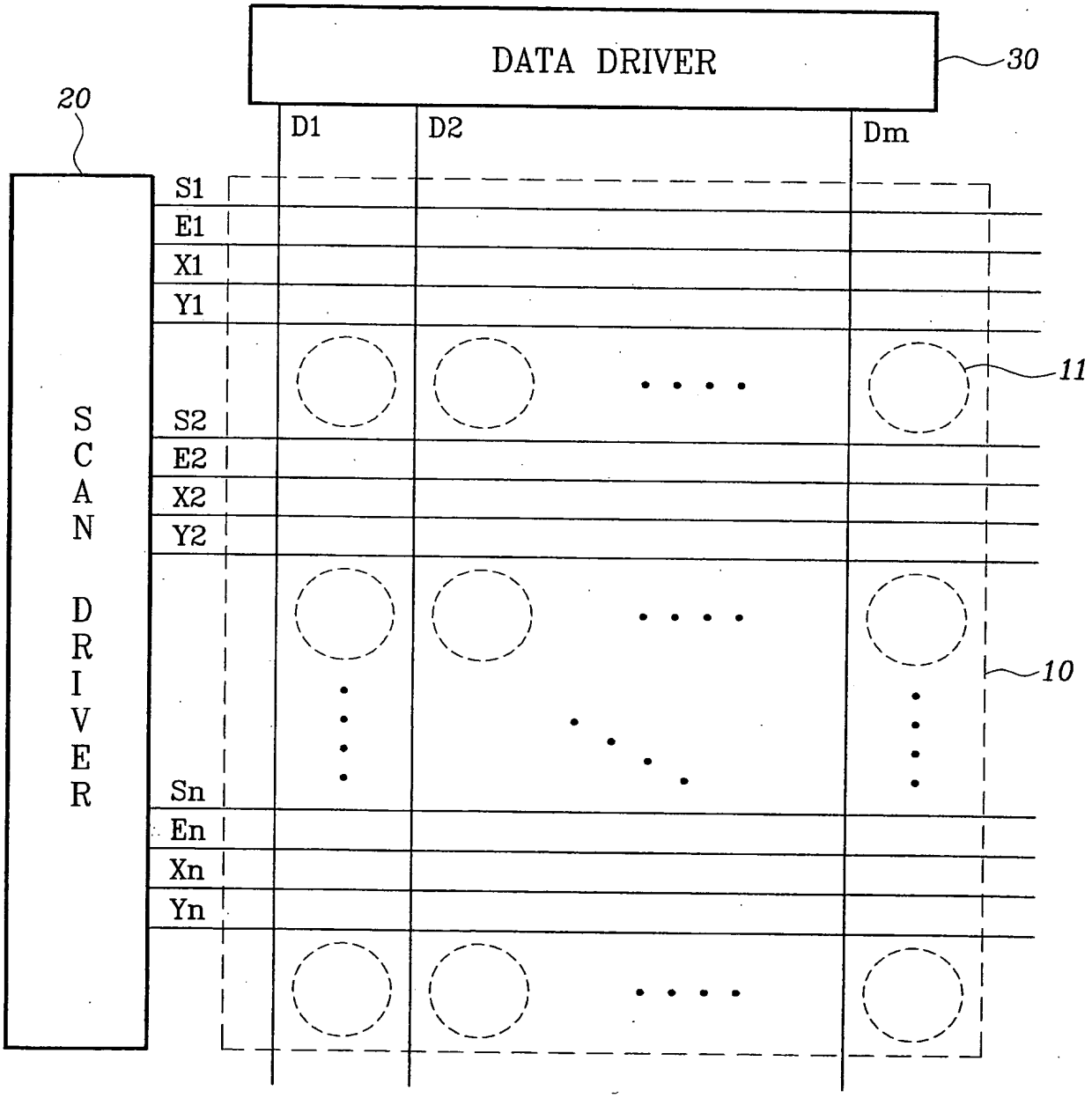

DATA DRIVER
30
20
D1
D2
Dm
S1
E1
X1
Y1
S2
E2
X2
Y2
Sn
En
Xn
Yn
SCAN DRIVER
11
10

FIG.5

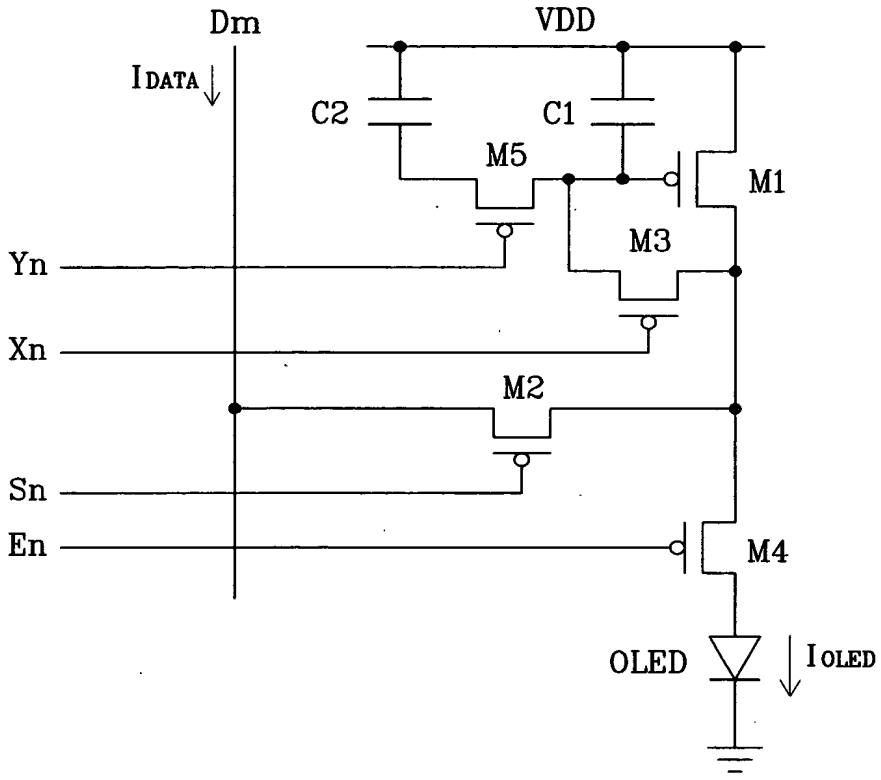

Dm
VDD
IDATA
C2
C1
M5
M1
M3
Yn
Xn
M2
Sn
En
M4
OLED
IOLED

FIG.6

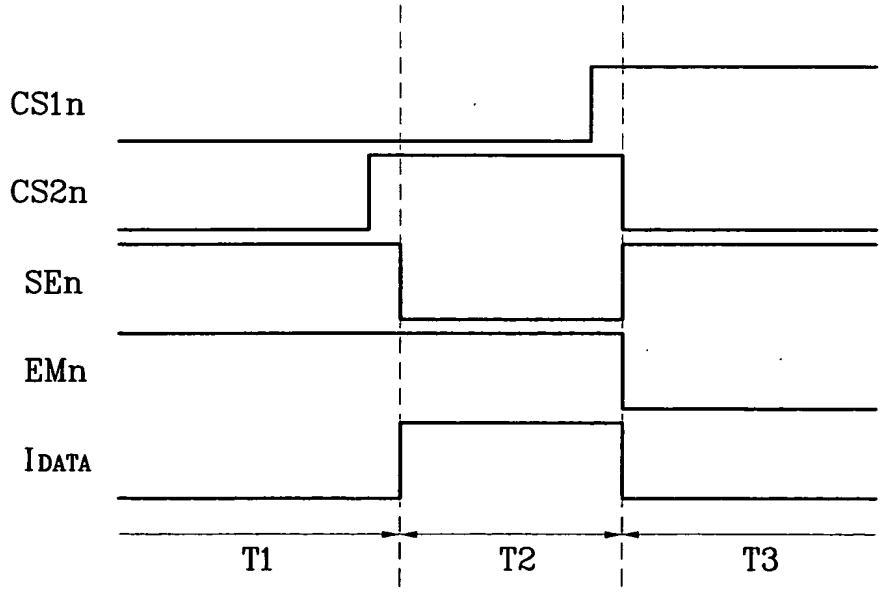

CS1n
CS2n
SEn
EMn
IDATA
T1
T2
T3

FIG. 7

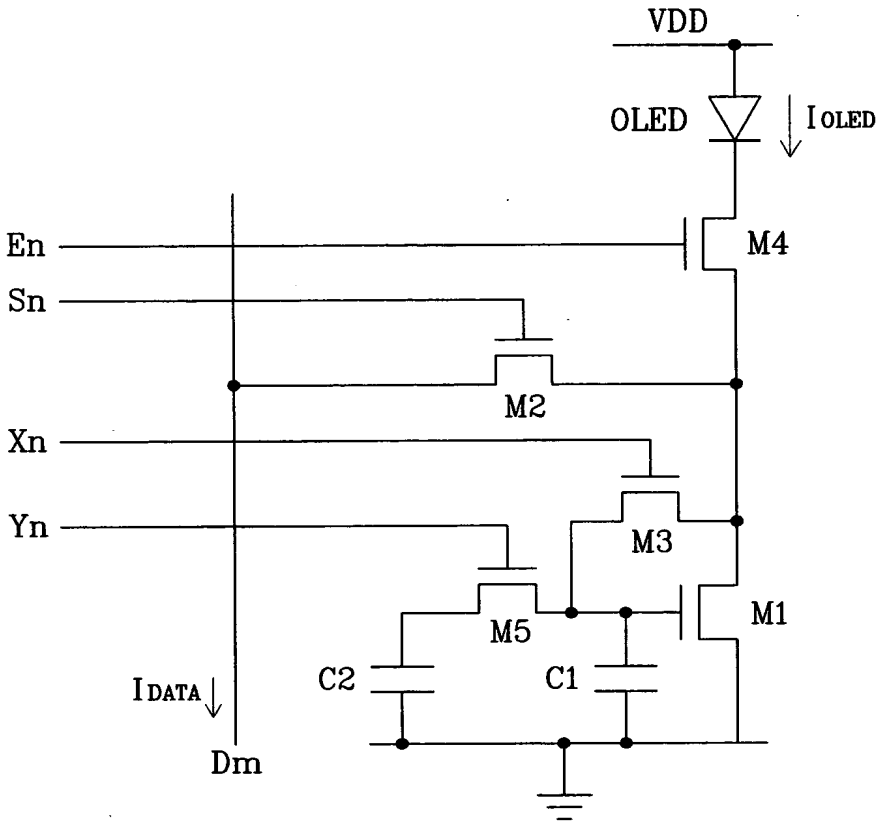

VDD
OLED
IOLED
En
Sn
Xn
Yn
M4
M2
M3
M5
M1
C2
C1
IDATA
Dm

FIG. 8

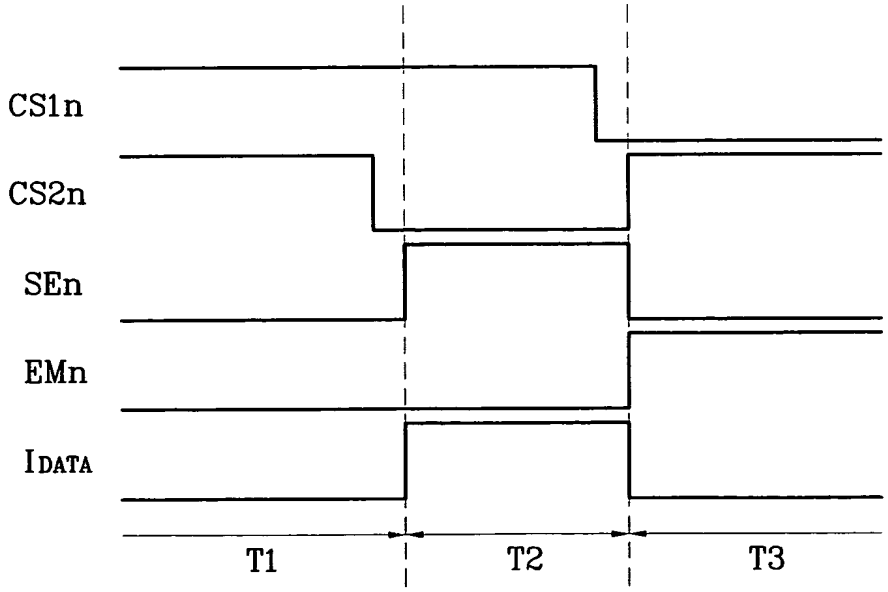

CS1n
CS2n
SEn
EMn
IDATA
T1
T2
T3

FIG.9

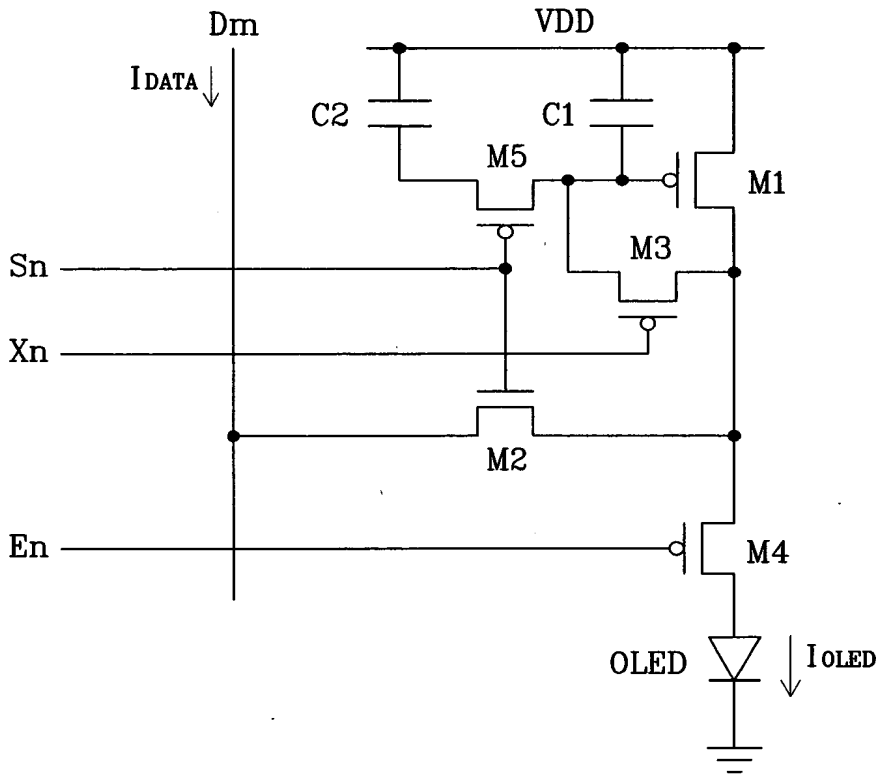

Dm
VDD
IDATA
C2
C1
M5
M1
M3
Sn
Xn
M2
En
M4
OLED
IOLED

FIG.10

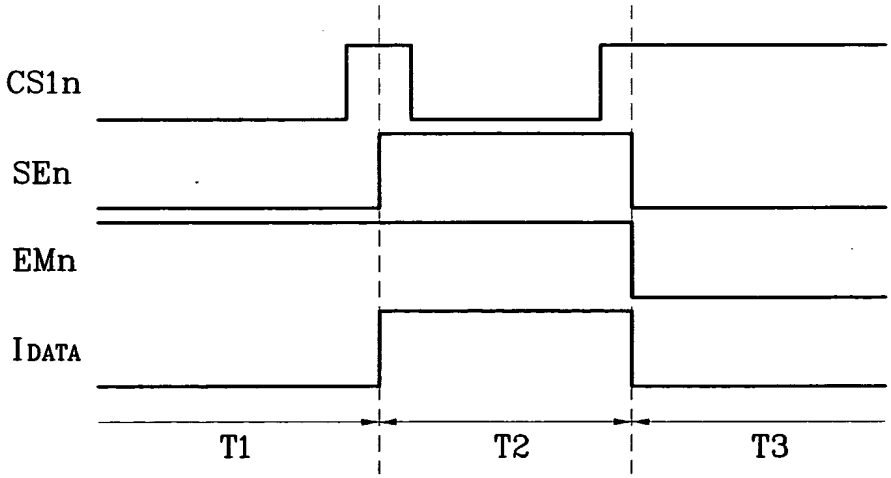

CS1n
SEn
EMn
IDATA
T1
T2
T3

FIG.11

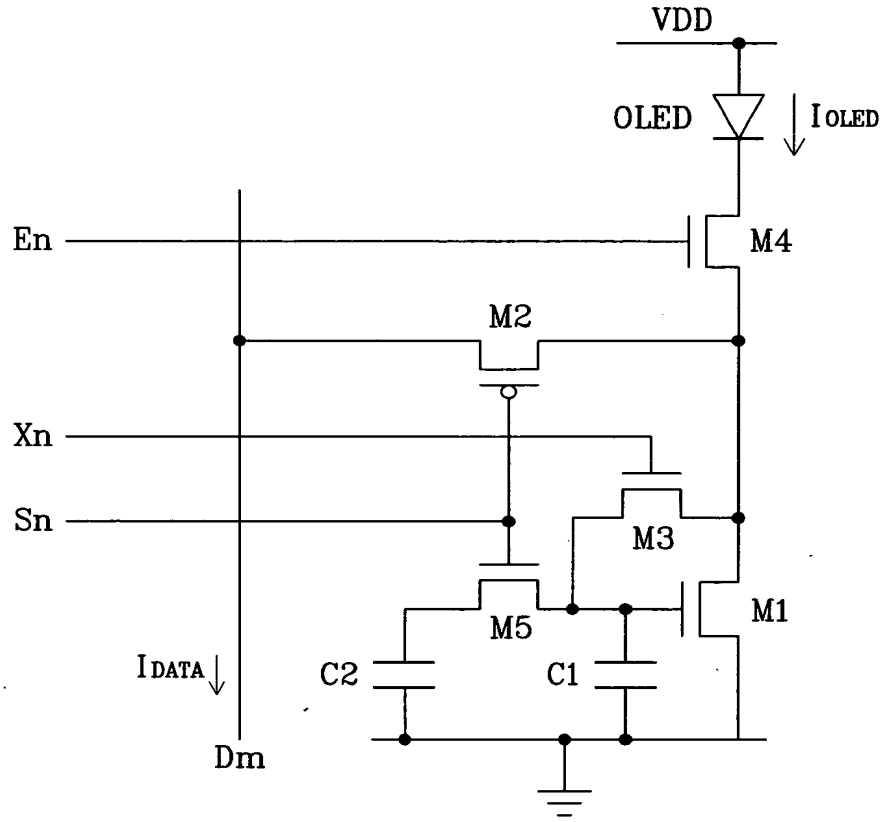

VDD
OLED
IOLED
En
M4
M2
Xn
Sn
M3
M1
M5
IDATA
C2
C1
Dm

FIG.12

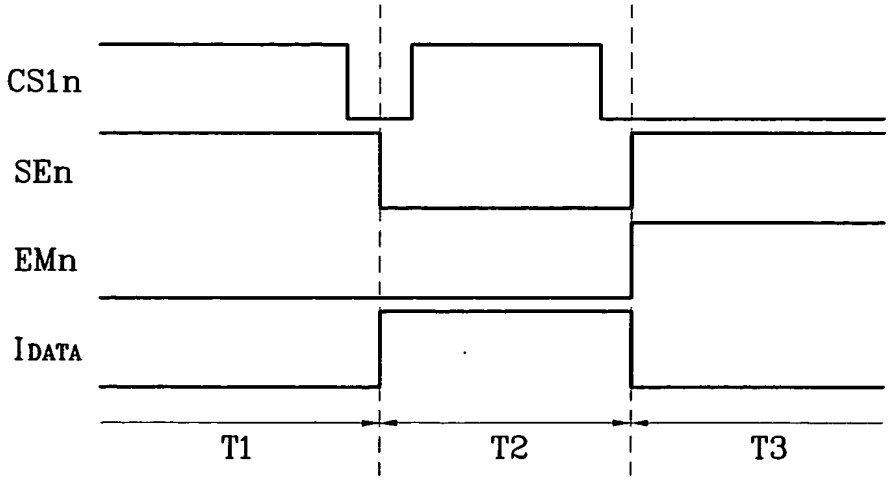

CS1n
SEn
EMn
IDATA
T1
T2
T3

FIG.13

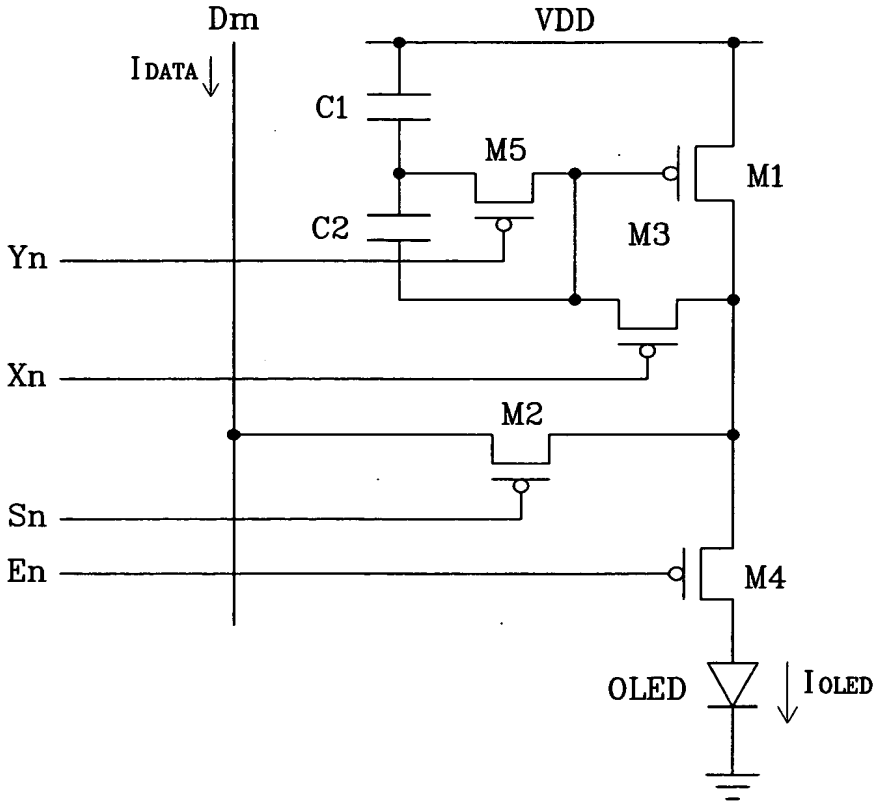


FIG.14

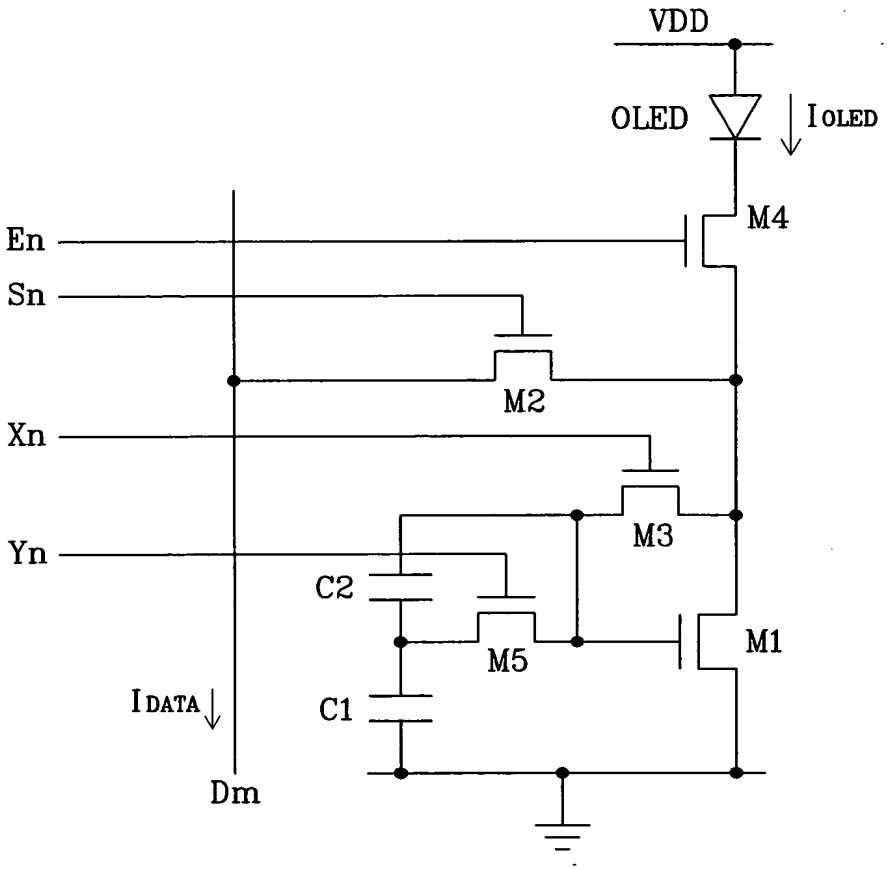


FIG.15

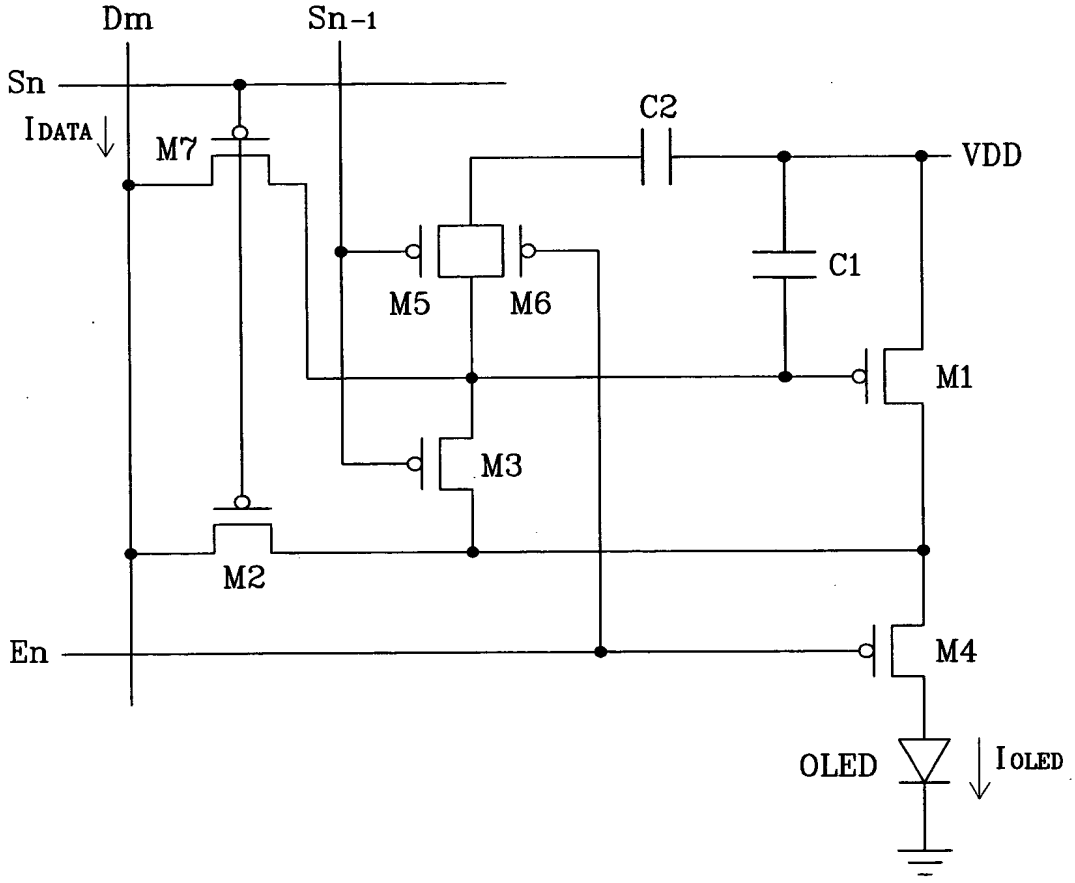
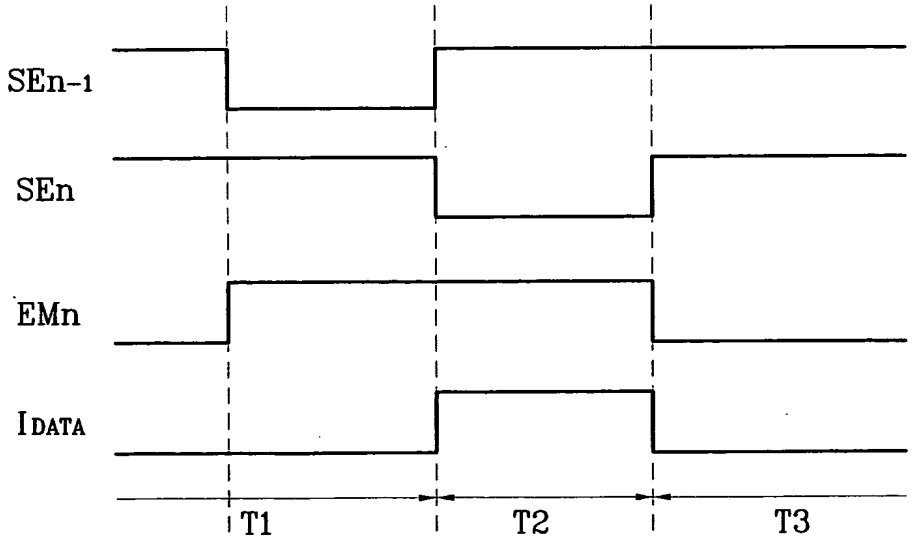

Dm
Sn-1
Sn
IDATA
M7
C2
VDD
C1
M5
M6
M1
M3
M2
En
M4
OLED
IOLED

FIG.16


SEn-1
SEn
EMn
IDATA
T1
T2
T3